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				•	Application Number	10/761 790	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)					Filing Date	01/21/2004	
					First Named Inventor	Albert E. Consano	<u>~</u>
					Art Unit	2819	
					Examiner Name	A Cosand Khai Naugen	
Sheet	1		of	11	Attorney Docket Number	PD-03W012	
			NC NC	ON PATENT	LITERATURE DOCUM	MENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), little of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					i, T²
kN		R. ADAMS, KHIEM NGUYEN & K. SWEETLAND, "A 113dB SNR Oversampling DAC with Segmented Noise-shaped Scrambling," IEEE International Symposium on Circuits and Systems, 1998, pp 62-64.					
KN		K.T. CHAN & K.W. MARTIN, "Components for a GAAS Delta-Sigma Modulator Oversampled Analog-To-Digital Converter," IEEE International Symposium on Circuits and Systems, 1992, pp 1300-1303.					

Examiner Signature	Nguyenkao	Date Considered 11/02/05	

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